

FIG.1A

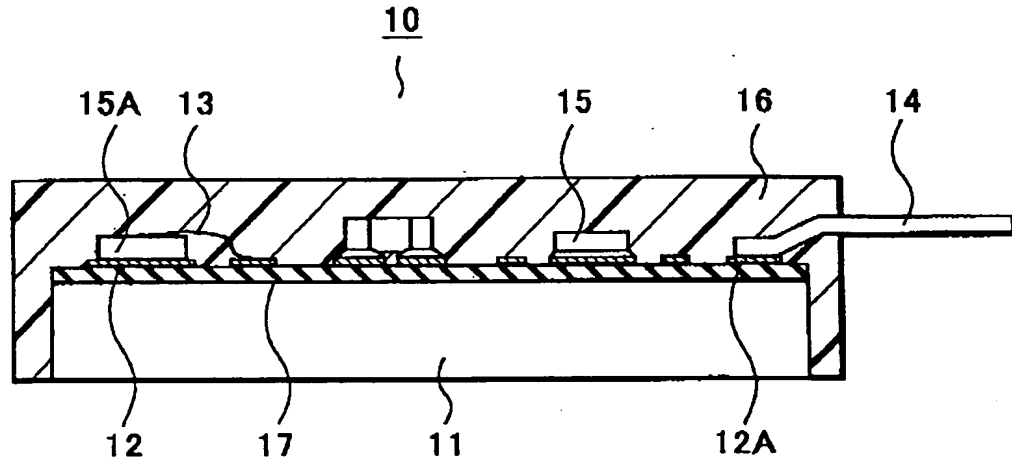


FIG.1B

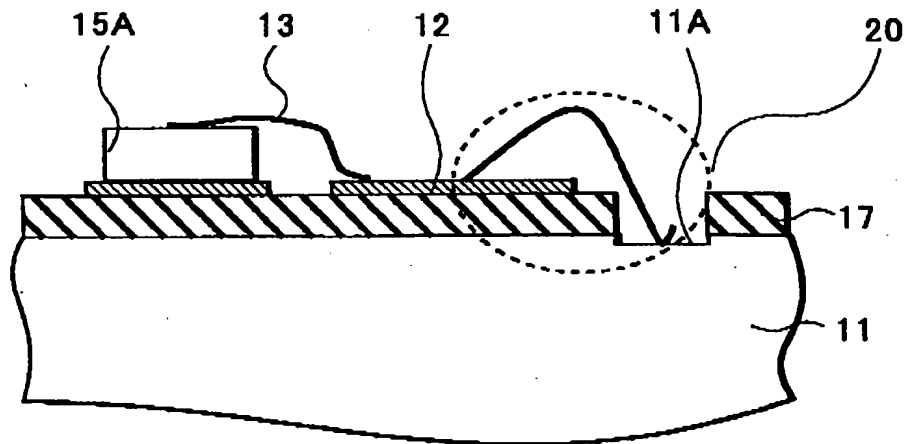
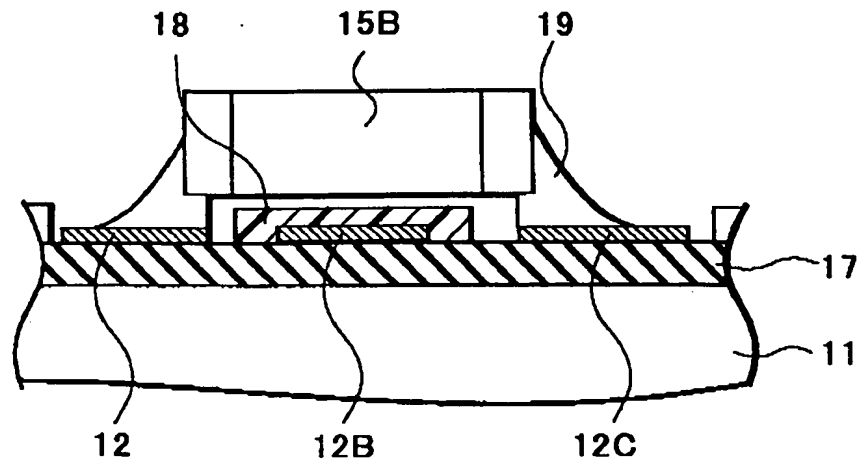
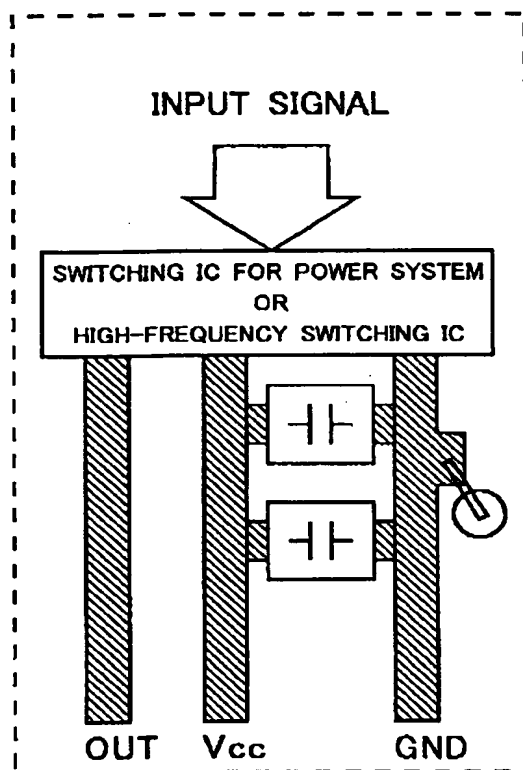


FIG.1C

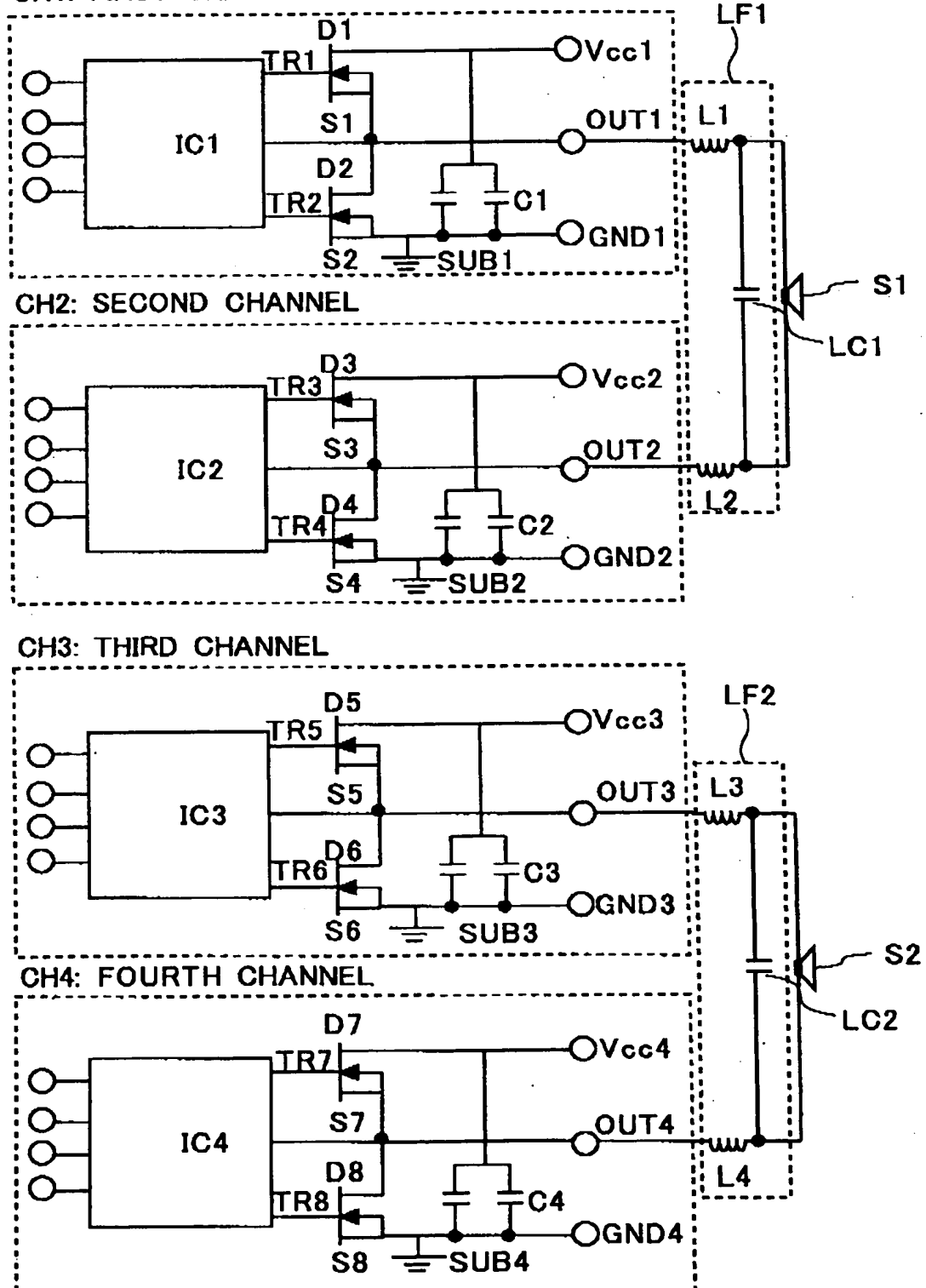


## CH2: SECOND CHANNEL

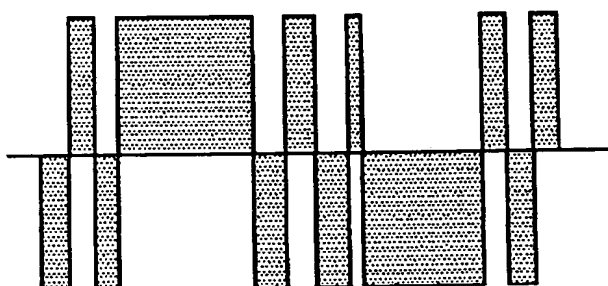


A block diagram of a switching IC for a power system or high-frequency switching IC. The diagram is enclosed in a dashed rectangular border. At the top, a large downward-pointing arrow is labeled "INPUT SIGNAL". Below the arrow is a rectangular block labeled "SWITCHING IC FOR POWER SYSTEM OR HIGH-FREQUENCY SWITCHING IC". Three vertical bars, representing pins, extend downwards from the bottom of the IC block. The leftmost pin is labeled "OUT" at its base. The middle pin is labeled "Vcc" at its base. The rightmost pin is labeled "GND" at its base. Between the middle and right pins, there are two square blocks, each containing a symbol consisting of two vertical lines and a horizontal line, representing a transistor or switch. A wavy line labeled "12" points to the top of the rightmost pin. A wavy line labeled "20" points to a circular component connected to the rightmost pin. A wavy line labeled "15B" points to the rightmost pin itself.

## CH1: FIRST CHANNEL



**FIG.4A**



**FIG.4B**

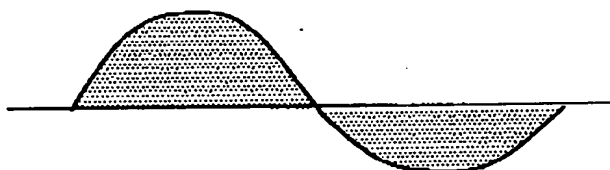


FIG.5A

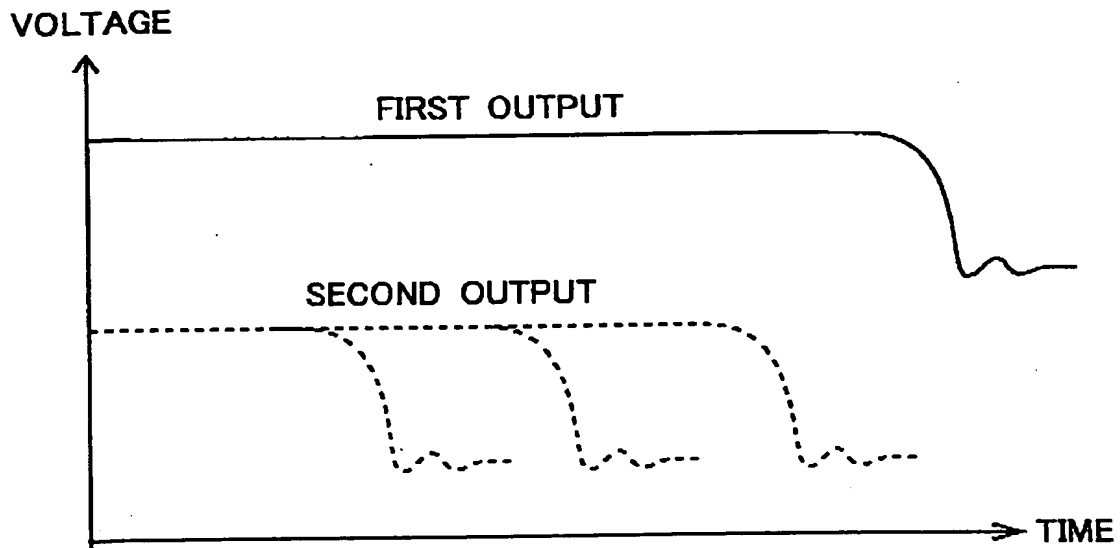


FIG.5B

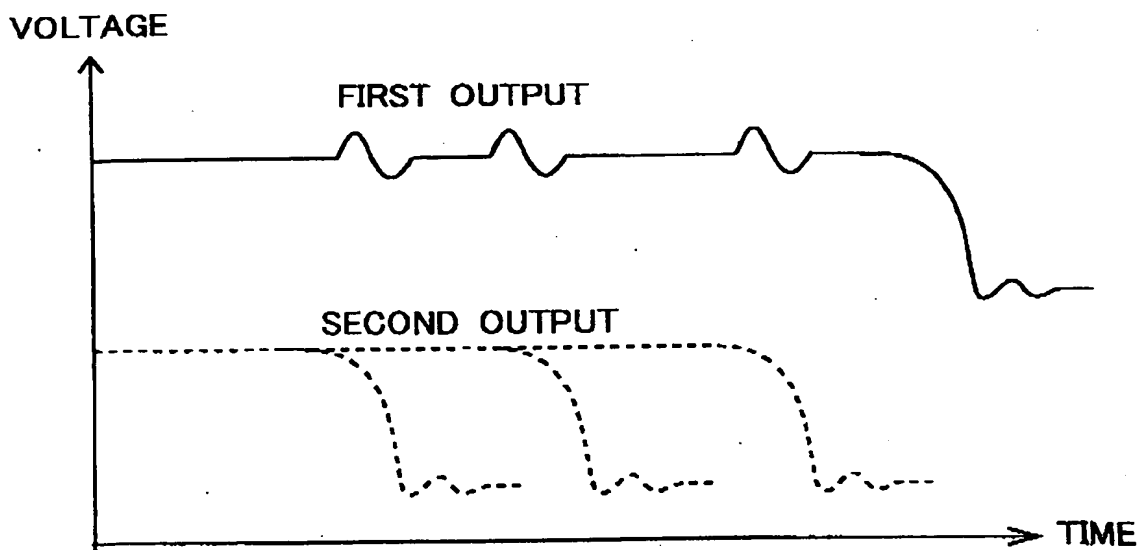


FIG.6A

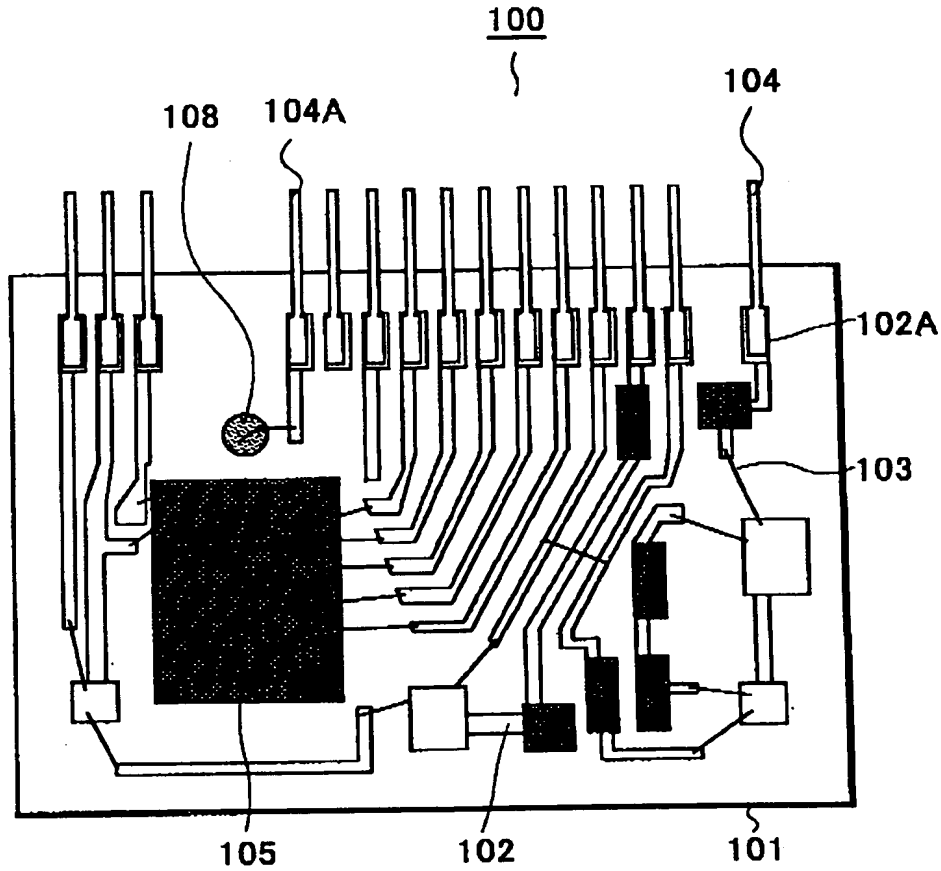


FIG.6B

